

**SYSTEM AND METHOD FOR REDUCING ZAP TIME AND TRACK
SQUEEZE IN A DATA STORAGE DEVICE**

Abstract of the Disclosure

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A method and apparatus for compensating for errors in servo systems. An improved zero acceleration path (ZAP) correction technique is provided, wherein selected tracks of a data storage device are used for ZAP processing in order to reduce the overall time required to perform error compensation for a storage device. For a given selected track to be ZAPed, track profiles of adjoining tracks are used in addition to the track profile of the selected track as part of the ZAP correction determination for the selected track. Using adjacent track profiles as part of the ZAP correction determination assists in mitigating AC track squeeze issues that would otherwise occur when performing selective track ZAPing.

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